2000-15/603	ND-39/40
j	$(\bar{q}_{j}$ a)
Ref	

COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:

STS FRAME-ATM CELL CIRCUIT EMULATION APPARATUS AND FRAME LENGTH COMPENSATION METHOD FOR THE SAME

the specification of which: (check one)

REGULAR OR DESIGN APPLICATION

	[X]	is attached hereto.	
र व्यक्त	[]	was filed on as application Serial No.	
ting that the transmission of the transmission		(if applicable).	
		PCT FILED APPLICATION ENTERING NATIONAL STAGE	
	[]	was described and claimed in International application No filed on	
: transl		and as amended on (if any).	
l hereby as ame	state that I handed by any ar	ve reviewed and understand the contents of the above-identified specification, including the claims, nendment referred to above.	
l ackno Regulat	wledge the dui	ty to disclose information which is material to patentability as defined in Title 37, Code of Federal	
===		PRIORITY CLAIM	
I hereby	claim foreign elow and have	priority benefits under 35 USC 119 of any foreign application(s) for patent or inventor's certificate also identified below any foreign application for patent or inventor's certificate having a filing date	

PRIOR FOREIGN APPLICATION(S)

Country	Application Number	Date of Filing (day, month, year)	Priority Claimed
Japan	2000-159603	30, 5, 2000	ves

(Complete this part only if this is a continuing application.)

before that of the application on which priority is claimed.

I hereby claim the benefit under 35 USC 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of 35 USC 112, I acknowledge the duty to disclose information which is material to patentability as defined in Title 37 Code of Federal Regulations §1.56 which became available between the filing date of the prior application and the national or PCT international filing date of this application:

(Application Serial No.)	(Filing Date)	(Statuspatented, pending, abandoned)
--------------------------	---------------	--------------------------------------

POWER OF ATTORNEY

The undersigned hereby authorizes the U.S. attorney or agent named herein to accept and follow instructions from <u>Harada Patent Office</u> as to any action to be taken in the Patent and Trademark Office regarding this application without direct communication between the U.S. attorney or agent and the undersigned. In the event of a change in the persons from whom instructions may be taken, the U.S. attorney or agent named herein will be so notified by the undersigned.

As a named inventor, I hereby appoint the following attorney(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith: Robert J. PATCH, Reg. No. 17,355, Andrew J. PATCH, Reg. No. 32,925, Robert F. HARGEST, Reg. No. 25,590, Benoît CASTEL, Reg. No. 35,041, Eric JENSEN, Reg. No. 37,855, and Thomas W. PERKINS, Reg. No. 33,027, c/o YOUNG & THOMPSON, Second Floor, 745 South 23rd Street, Arlington, Virginia 22202.

Address all telephone calls to Young & Thompson at 703/521-2297.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name of sole or first inventor:					
(given name, family name)					
Inventor's signature Souichi Kataoka	Date May 22, 2001				
Souichi KATAOKA	,				
Résidence: Tokyo, Japan	Citizenship: Japānese				
Post Office Address: c/o NEC Communication Systems, Ltd., 4-28, Mita 1-chome, Minato-ku, Tokyo, Japan					
Full name of second joint inventor, if any:					
Inventor's signature Ken Shiraishi	Date May 22, 2001				
Ken SHIRAISHI Residence: Tokyo, Japan	Citizenship: Japanese				
Post Office Address: c/o NEC Corporation, 7-1, Shiba 5-chome, Minato-ku, Tokyo, Japan					
Full name of third joint inventor, if any: (given name, family name)					
Inventor's signature	Date				
Residence:	Citizenship:				
Post Office Address:					